## Product Specification

## General Description

QSFP+ Direct Attach Cables are compliant with the SFF-8436 specifications. Various choices of wire gauge are available from 30 to 24 AWG with various choices of cable length (up to 7 m ).

## Features

- Compliant with SFF- 8436.
- Up to 10.3125 Gbps data rate per channel
- Up to 7 m transmission
- Operating temperature: $-40^{\circ} \mathrm{C}$ to $+80^{\circ} \mathrm{C}$
- Single 3.3V power supply
- RoHS compliant


## Benefits

- Cost-effective copper solution
- Lowest total system power solution
- Lowest total system EMI solution
- Optimized design for Signal Integrity


## Applications

- 40G Ethernet

Pin Function Definition

| Pin | Logic | Symbol | Description |
| :---: | :---: | :---: | :---: |
| 1 |  | GND | Ground |
| 2 | CML-I | Tx2n | Transmitter Inverted Data Input |
| 3 | CML-I | Tx2p | Transmitter Non-Inverted Data Input |
| 4 |  | GND | Ground |
| 5 | CML-I | Tx4n | Transmitter Inverted Data Input |
| 6 | CML-I | Tx4p | Transmitter Non-Inverted Data Input |

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| 7 |  | GND | Ground |
| :---: | :---: | :---: | :---: |
| 8 | LVTTL-I | ModSelL | Module Select |
| 9 | LVTTL-I | ResetL | Module Reset |
| 10 |  | Vcc Rx | +3.3V Power Supply Receiver |
| 11 | $\begin{gathered} \text { LVCMOS- } \\ \text { I/O } \end{gathered}$ | SCL | 2-wire serial interface clock |
| 12 | $\begin{gathered} \hline \text { LVCMOS- } \\ \text { I/O } \\ \hline \end{gathered}$ | SDA | 2-wire serial interface data |
| 13 |  | GND | Ground |
| 14 | CML-O | Rx3p | Receiver Non-Inverted Data Output |
| 15 | CML-O | Rx3n | Receiver Inverted Data Output |
| 16 |  | GND | Ground |
| 17 | CML-O | Rx1p | Receiver Non-Inverted Data Output |
| 18 | CML-O | Rx1n | Receiver Inverted Data Output |
| 19 |  | GND | Ground |
| 20 |  | GND | Ground |
| 21 | CML-O | Rx2n | Receiver Inverted Data Output |
| 22 | CML-O | Rx2p | Receiver Non-Inverted Data Output |
| 23 |  | GND | Ground |
| 24 | CML-O | Rx4n | Receiver Inverted Data Output |
| 25 | CML-O | Rx4p | Receiver Non-Inverted Data Output |
| 26 |  | GND | Ground |
| 27 | LVTTL-O | ModPrsL | Module Present |
| 28 | LVTTL-O | IntL | Interrupt |
| 29 |  | Vcc Tx | +3.3V Power supply transmitter |
| 30 |  | Vcc1 | +3.3V Power supply |
| 31 | LVTTL-I | LPMode | Low Power Mode |
| 32 |  | GND | Ground |
| 33 | CML-I | Tx3p | Transmitter Non-Inverted Data Input |
| 34 | CML-I | Tx3n | Transmitter Inverted Data Input |
| 35 |  | GND | Ground |
| 36 | CML-I | Tx1p | Transmitter Non-Inverted Data Input |
| 37 | CML-I | Tx1n | Transmitter Inverted Data Input |
| 38 |  | GND | Ground |



General Product Characteristics

| QSFP+ DAC Specifications |  |
| :---: | :---: |
| Number of Lanes | $\mathrm{Tx} \& \mathrm{Rx}$ |
| Channel Data Rate | 10.3125 Gbps |
| Operating Temperature | 0 to $+70^{\circ} \mathrm{C}$ |
| Storage Temperature | -40 to $+85^{\circ} \mathrm{C}$ |
| Supply Voltage | 3.3 V nominal |
| Electrical Interface | 38 pins edge connector |
| Management Interface | Serial, I2C |

High Speed Characteristics

| Parameter | Symbol | Min | Typical | Max | Unit | Note |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Differential Impedance | TDR | 90 | 100 | 110 | ' $\Omega$ |  |
| Insertion loss | SDD21 | -17.04 |  | -3 | dB | At 5.15625 GHz |
| Differential Return Loss | $\begin{aligned} & \text { SDD11 } \\ & \text { SDD22 } \end{aligned}$ |  |  | See 1 | dB | At 0.05 to 4.1 GHz |
|  |  |  |  | See 2 | dB | At 4.1 to 11.1 GHz |
| Differential to common-mode return loss | $\begin{aligned} & \text { SCD11 } \\ & \text { SCD22 } \end{aligned}$ |  |  | -10 | dB | At 0.2 to 11.1 GHz |
| Common-mode to common-mode output return loss | $\begin{aligned} & \text { SCC11 } \\ & \text { SCC22 } \end{aligned}$ |  |  | See 3 | dB | At 0.01 to 2.5 GHz |
|  |  |  |  | -3 |  | At 2.5 GHz to 11.1 GHz |
| Channel Operating Margin | COM | 3 |  |  | dB |  |

## Notes:

1.Reflection Coefficient given by equation SDD11(dB) $<-\mathbf{1 2}+2 \times \operatorname{SQRT}(f)$, with $\mathbf{f}$ in $\mathbf{G H z}$
2. Reflection Coefficient given by equation SDD11(dB) $<-6.3+13 \times \log 10(f / 5.5)$, with $f$ in $\mathbf{G H z}$
3.Reflection Coefficient given by equation $\operatorname{SCC11(dB)}$ < -7+1.6*f, with $\mathbf{f}$ in $\mathbf{G H z}$

## MechanicalSpecifications

The connector is compatible with the SFF-8436 specification.


| Length (m) | Cable AWG |
| :---: | :---: |
| 1 | 30 |
| 3 | 30 |
| 5 | 26 |
| 7 | 26 |

## Regulatory Compliance

| Feature | Test Method | Performance |
| :---: | :---: | :---: |
| Electrostatic Discharge (ESD) to the <br> Electrical Pins | MIL-STD-883C Method 3015.7 | Class 1(>2000 Volts) |
|  | FCC Class B | Compliant with Standards |
|  | CENELEC EN55022 Class B |  |
|  | CISPR22 ITE Class B | Typically Show no Measurable Effect <br> from a 10V/m Field Swept from 80 to <br> 1000 MHz |
| RF Immunity(RFI) | IEC61000-4-3 | RoHS 6/6 compliant |
| RoHS Compliance | RoHS Directive 2011/65/EU and it's Amendment <br> Directives 6/6 |  |

