

General Description

QSFP28 Direct Attach Cables are compliant with the SFF-8665 specifications. Various choices of wire gauge are available from 30 to 26 AWG with various choices of cable length (up to 5m).

Features

- Compliant with SFF- 8665
- Up to 28.3125Gbps data rate per channel
- Up to 5m transmission
- Operating temperature: 0~70°C
- Single 3.3V power supply
- RoHS compliant

Benefits

- Cost-effective copper solution
- Lowest total system power solution
- Lowest total system EMI solution
- Optimized design for Signal Integrity

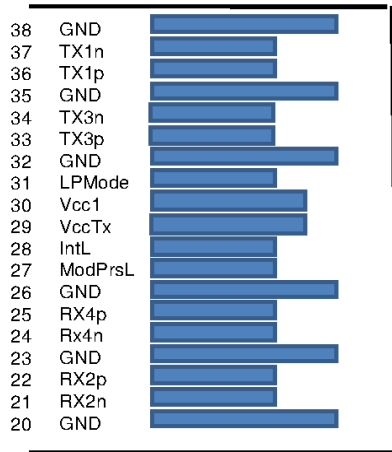
Applications

- 100G Ethernet

Pin Function Definition

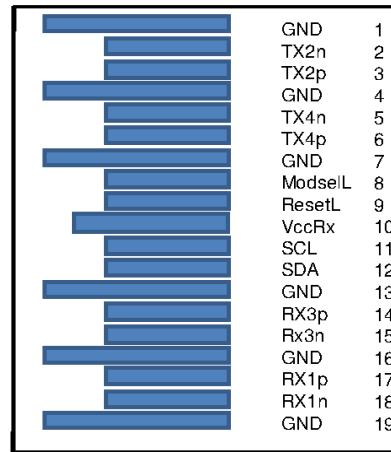
Pin	Logic	Symbol	Description
1		GND	Ground
2	CML-I	Tx2n	Transmitter Inverted Data Input
3	CML-I	Tx2p	Transmitter Non-Inverted Data Input
4		GND	Ground
5	CML-I	Tx4n	Transmitter Inverted Data Input

6	CML-I	Tx4p	Transmitter Non-Inverted Data Input
7		GND	Ground
8	LVTTL-I	ModSelL	Module Select
9	LVTTL-I	ResetL	Module Reset
10		Vcc Rx	+3.3V Power Supply Receiver
11	LVC MOS-I/O	SCL	2-wire serial interface clock
12	LVC MOS-I/O	SDA	2-wire serial interface data
13		GND	Ground
14	CML-O	Rx3p	Receiver Non-Inverted Data Output
15	CML-O	Rx3n	Receiver Inverted Data Output
16		GND	Ground
17	CML-O	Rx1p	Receiver Non-Inverted Data Output
18	CML-O	Rx1n	Receiver Inverted Data Output
19		GND	Ground
20		GND	Ground
21	CML-O	Rx2n	Receiver Inverted Data Output
22	CML-O	Rx2p	Receiver Non-Inverted Data Output
23		GND	Ground
24	CML-O	Rx4n	Receiver Inverted Data Output
25	CML-O	Rx4p	Receiver Non-Inverted Data Output
26		GND	Ground
27	LVTTL-O	ModPrsL	Module Present
28	LVTTL-O	IntL	Interrupt
29		Vcc Tx	+3.3V Power supply transmitter
30		Vcc1	+3.3V Power supply
31	LVTTL-I	LPMODE	Low Power Mode
32		GND	Ground
33	CML-I	Tx3p	Transmitter Non-Inverted Data Input
34	CML-I	Tx3n	Transmitter Inverted Data Input
35		GND	Ground
36	CML-I	Tx1p	Transmitter Non-Inverted Data Input
37	CML-I	Tx1n	Transmitter Inverted Data Input
38		GND	Ground



Top Side
Viewed From Top

Module Card Edge



Bottom Side
Viewed From Bottom

General Product Characteristics

QSFP+ DAC Specifications	
Number of Lanes	Tx & Rx
Channel Data Rate	28.3125 Gbps
Operating Temperature	0 to + 70°C
Storage Temperature	-40 to + 85°C
Supply Voltage	3.3 V nominal
Electrical Interface	38 pins edge connector
Management Interface	Serial, I2C

High Speed Characteristics

Parameter	Symbol	Min	Typical	Max	Unit	Note
Differential Impedance	TDR	90	100	110	Ω	
Insertion loss	SDD21	-22.48		-8	dB	At 12.8906 GHz
Differential Return Loss	SDD11 SDD22	-12.45		See 1	dB	At 0.05 to 4.1 GHz
		-3.12		See 2	dB	At 4.1 to 19 GHz
Common-mode to common-mode output return loss	SCC11 SCC22			-2	dB	At 0.2 to 19 GHz
Differential to common-mode return loss	SCD11 SCD22	-12		See 3	dB	At 0.01 to 12.89 GHz
		-10.58		See 4		At 12.89 to 19 GHz
Differential to common Mode Conversion Loss	SCD21-IL			-10	dB	At 0.01 to 12.89 GHz
				See 5		At 12.89 to 15.7 GHz
				-6.3		At 15.7 to 19 GHz
Channel Operating Margin	COM			-3	dB	

Notes:

Reflection Coefficient given by equation $SDD11(dB) < -16.5 + 2 \times \text{SQRT}(f)$, with f in GHz

Reflection Coefficient given by equation $SDD11(dB) < -10.66 + 14 \times \log_{10}(f/5.5)$, with f in GHz

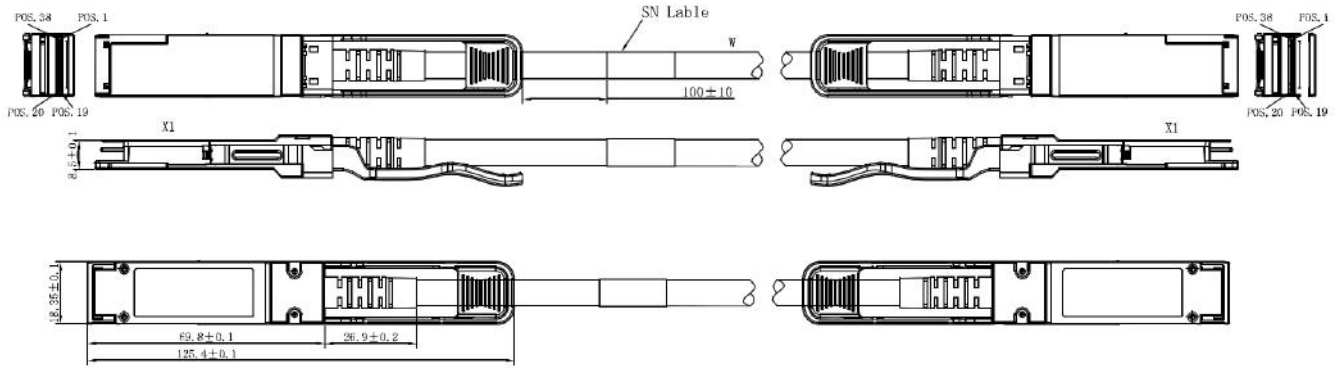
Reflection Coefficient given by equation $SCD11(dB) < -22 + (20/25.78)*f$, with f in GHz

Reflection Coefficient given by equation $SCD11(dB) < -15 + (6/25.78)*f$, with f in GHz

Reflection Coefficient given by equation $SCD21(dB) < -27 + (29/22)*f$, with f in GHz

Mechanical Specifications

The connector is compatible with the SFF-8665 specification.



Length (m)	Cable AWG
1	30
2	30
3	30
5	26

Regulatory Compliance

Feature	Test Method	Performance
Electrostatic Discharge (ESD) to the Electrical Pins	MIL-STD-883C Method 3015.7	Class 1(>2000 Volts)
Electromagnetic Interference(EMI)	FCC Class B	Compliant with Standards
	CENELEC EN55022 Class B	
	CISPR22 ITE Class B	
RF Immunity(RFI)	IEC61000-4-3	Typically Show no Measurable Effect from a 10V/m Field Swept from 80 to 1000MHz
RoHS Compliance	RoHS Directive 2011/65/EU and it's Amendment Directives 6/6	RoHS 6/6 compliant