

GJS5L0CCM

SFP28 25G CWDM(1270-1330) 40KM Optical Transceiver

Features

- Supports up to 25.78Gbps bit rates
- Hot-pluggable SFP+ footprint
- CWDM DFB laser and PIN photodiode, Up to 10km for SMF transmission
- Compliant with SFP+ MSA and SFF-8472 with duplex LC receptacle
- Compatible with RoHS
- Single +3.3V power supply
- Real Time Digital Diagnostic Monitoring
- Operating case temperature: Standard: 0 to +70° C

Application

- 25GBASE-LR

Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit	Notes
Supply Voltage	V _{cc}	-0.5	4.5	V	
Storage Temperature	T _s	-40	+85	°C	
Operating Humidity	-	5	85	%	

Recommended Operating Conditions

Parameter	Symbol	Min.	Typical	Max.	Unit	Notes
Operating Case Temperature	T _c	0	-	+70	°C	
Power Supply Voltage	V _{cc}	3.135	3.3	3.465	V	
Power Supply Current	I _{cc}	-	-	400	MA	
Data Rate		-	25.78	-	Gbps	

Optical and Electrical Characteristics

Parameter	Symbol	Min	Typical	Max	Unit	Notes
Transmitter						
Centre Wavelength	λ_c	$\lambda_c-6.5$	λ_c	$\lambda_c+6.5$	nm	
Spectral Width (-20dB)	$\Delta\lambda$			1	nm	
Side-Mode Suppression Ratio	SMSR	30	-		dB	
Average Output Power	Pout	-4		4	dBm	1
Extinction Ratio	ER	3.5			dB	
Data Input Swing Differential	VIN	180		850	mV	2
Input Differential Impedance	ZIN	90	100	110	Ω	
TX Disable	Disable		2.0	Vcc	V	
	Enable		0	0.8	V	
TX Fault	Fault		2.0	Vcc	V	
	Normal		0	0.8	V	
Receiver						
Centre Wavelength	λ_c	1260		1620	nm	
Receiver Sensitivity				-13.3	dBm	3
Receiver Overload				2	dBm	3
LOS De-Assert	LOSD			-15	dBm	
LOS Assert	LOSA	-30			dBm	
LOS Hysteresis		0.5			dB	
Data Output Swing Differential	Vout	300		900	mV	4
LOS	High	2.0		Vcc	V	
	Low			0.8	V	

Notes:

1. The optical power is launched into SMF.
1. PECL input, internally AC-coupled and terminated.
2. Measured with a PRBS 231-1 test pattern @25.78Gps, BER $\leq 5 \times 10^{-5}$.
3. Internally AC-coupled.

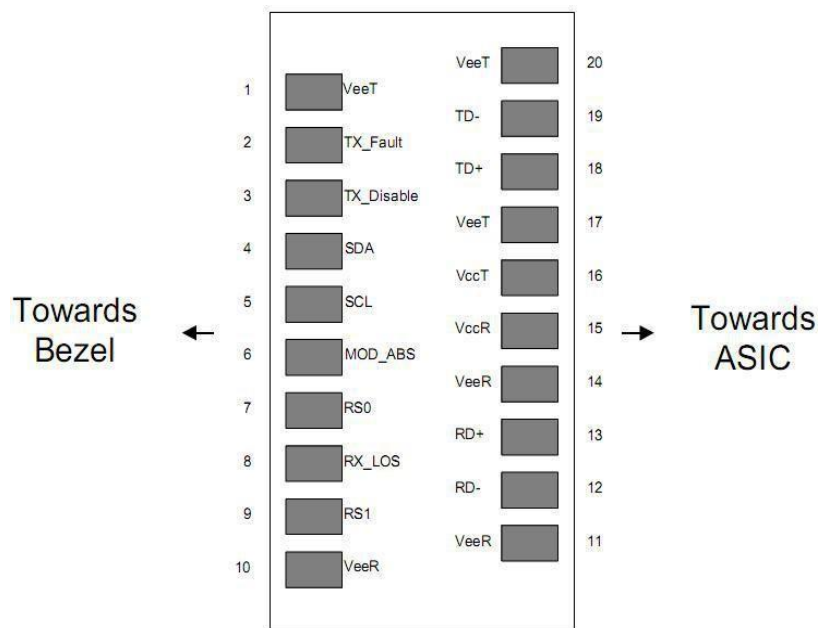
Timing and Electrical

Parameter	Symbol	Min	Typical	Max	Unit
Tx Disable Negate Time	t_on			2	ms
Tx Disable Assert Time	t_off			100	μ s
Time To Initialize, including Reset of	t_init			300	ms
Tx Fault Assert Time	t_fault			100	μ s
Tx Disable To Reset	t_reset	10			μ s
LOS Assert Time	t_loss_on			100	μ s
LOS De-assert Time	t_loss_off			100	μ s
Serial ID Clock Rate	f_serial_clock		100	400	KHz
MOD_DEF (0:2)-High	V _H	2		Vcc	V
MOD_DEF (0:2)-Low	V _L			0.8	V

Diagnostics

Parameter	Range	Unit	Accuracy	Calibration
Temperature	0 to+ 70	°C	±3°C	Internal
Voltage	3.0 to 3.6	V	±3%	Internal
Bias Current	0 to 100	mA	±10%	Internal
Power	-4 to 4	dBm	±3dB	Internal
Rx Power	-14 to +2	dBm	±3dB	Internal

Pin Descriptions



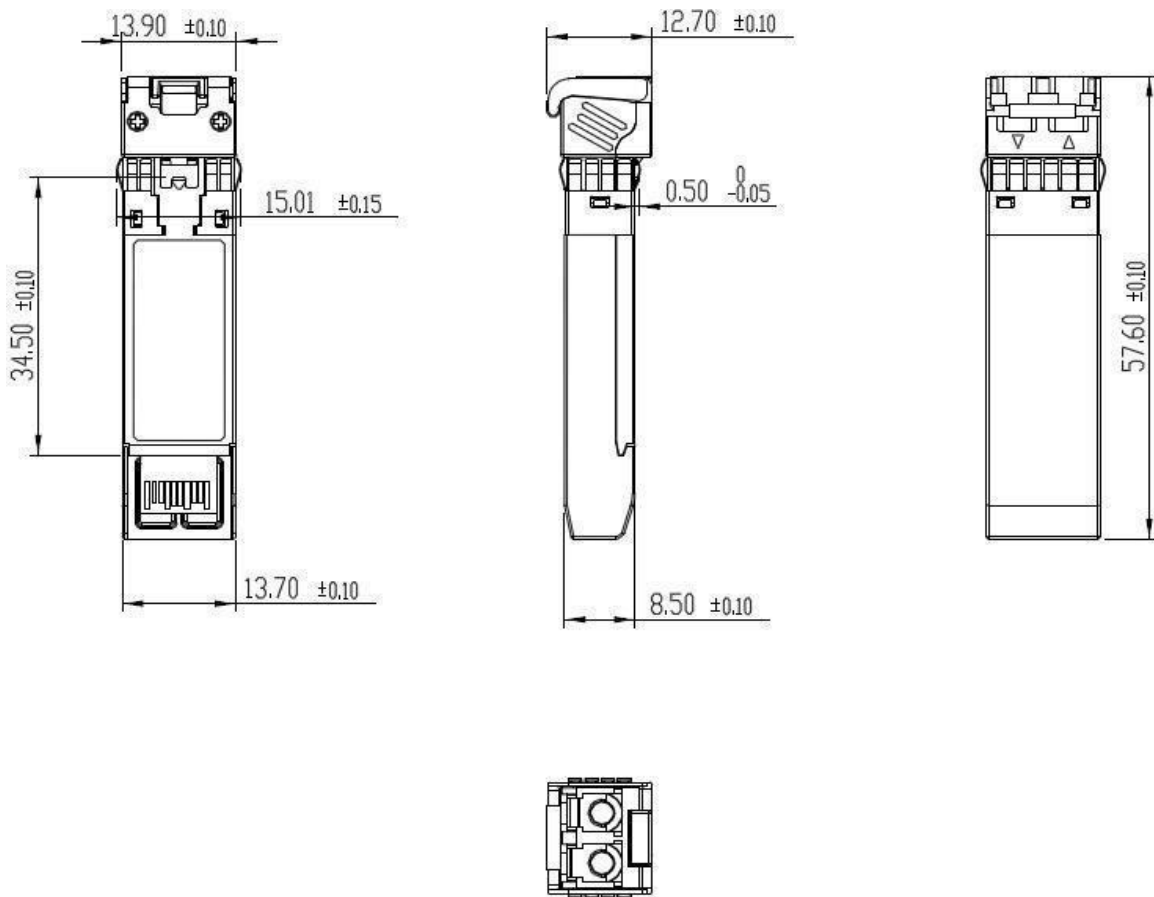
Pin	Signal Name	Description	Plug Seq.	Notes
1	V _{EET}	Transmitter Ground	1	
2	TX FAULT	Transmitter Fault Indication	3	Note 1
3	TX DISABLE	Transmitter Disable	3	Note 2
4	SDA	SDA Serial Data Signal	3	
5	SCL	SCL Serial Clock Signal	3	
6	MOD_ABS	Module Absent. Grounded within the module	3	
7	RS0	Not Connected	3	
8	LOS	Loss of Signal	3	Note 3
9	RS1	Not Connected	3	
10	V _{EER}	Receiver ground	1	
11	V _{EER}	Receiver ground	1	
12	RD-	Inv. Received Data Out	3	Note 4
13	RD+	Received Data Out	3	Note 4
14	V _{EER}	Receiver ground	1	

15	V _{CCR}	Receiver Power Supply	2	
16	V _{CCT}	Transmitter Power Supply	2	
17	V _{EET}	Transmitter Ground	1	
18	TD+	Transmit Data In	3	Note 5
19	TD-	Inv. Transmit Data In	3	Note 5
20	V _{EET}	Transmitter Ground	1	

Notes: Plug Seq.: Pin engagement sequence during hot plugging.

1. TX Fault is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor on the host board to a voltage between 2.0V and V_{cc}+0.3V. Logic 0 indicates normal operation; Logic 1 indicates a laser fault of some kind. In the low state, the output will be pulled to less than 0.8V.
2. Laser output disabled on TDIS >2.0V or open, enabled on TDIS <0.8V.
3. LOS is open collector output. Should be pulled up with 4.7k~10kΩ on host board to a voltage between 2.0V and 3.6V. Logic 0 indicates normal operation; logic 1 indicates loss of signal.
4. RD-/+: These are the differential receiver outputs. They are internally AC-coupled 100 differential lines which should be terminated with 100Ω (differential) at the user SERDES.
5. TD-/+: These are the differential transmitter inputs. They are internally AC-coupled, differential lines with 100Ω differential termination inside the module

Mechanical Dimension



Ordering Information

Part Number	Product Description
GHS5L0CCM	1270~1610nm CWDM, 25.78Gbps, LC, 10km, 0°C~+70°C, with DDM